

**INFORMATION DISCLOSURE STATEMENT PURSUANT TO**  
**37 C.F.R. §§1.97-1.99**

***PATENT APPLICATION***

Applicant(s): Kai D. Feng *et al.*

Docket No.: END920030078US1

**FOR: GLITCH-FREE RECEIVERS FOR BI-DIRECTIONAL, SIMULTANEOUS DATA BUS**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure under 37 C.F.R. §1.56 and pursuant to 37 C.F.R. §§1.97-1.99, Applicant(s) hereby notifies the U.S. Patent and Trademark Office of the documents listed on the attached Form PTO-1449. Applicant respectfully submits that all pending claims are patentable over the foregoing references, alone or in combination. The Examiner is requested to initial the enclosed Form PTO-1449 and return a copy thereof to the undersigned.

The submission of the listed documents is not intended as an admission that any such document constitutes prior art against the claims of the present application. Applicant reserves the right to dispute any of the listed documents as prior art during examination. Further, Applicant does not waive any right to take any action that would be appropriate to antedate or otherwise remove any listed document as a competent reference against the claims of the present application. Furthermore, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made or that no other material information may exist. This Information Disclosure Statement is being filed within three months of the filing date of the captioned patent application, and therefore no certification under 37 C.F.R. §1.97(e) or fee under 37 C.F.R. §1.17(p) is required.

Respectfully submitted,

Dated: 10/15/2003

By Jack P. Friedman  
Jack P. Friedman  
Reg. No. 44,688

Enclosures: PTO-1449

# **INFORMATION DISCLOSURE CITATION**

*(Use several sheets if necessary)*

ATTY DOCKET NO.  
END920030078US1

SERIAL NO.

Kai D. Feng et al.

FILING

GROUP

## **U.S. PATENT DOCUMENTS**

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	2001/0048341	12/2001	Chakravarthy			
	6,535,057	3/2003	Chakravarthy			
	6,405,228	6/2002	Williams et al.			
	6,144,786	11/2000	Chethik			
	6,025,744	2/2000	Bertolet et al.			
	5,467,041	11/1995	Baba et al.			
	5,289,060	2/1994	Elnashar et al.			
	4,105,980	8/1978	Cowardin et al.			
	5,604,450	2/1997	Borkar et al.			

## **FOREIGN PATENT DOCUMENTS**

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
	63-254827	10/1988	Japan				
	04-213922	8/1992	Japan				
	04-259114	9/1992	Japan				

## **OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**

		Mooney et al., A 900 Mb/s BIDIRECTIONAL SIGNALING SCHEME; IEEE Journal of Solid-State Circuits, Vol. 30, No. 12, December 1995, pages 1538-1543.

EXAMINER

DATE CONSIDERED

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.